

# CSC/WCPM joint seminar

## Scientific Computing on Dataflow Engines

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Monday, 21<sup>st</sup> November, 1 p.m.

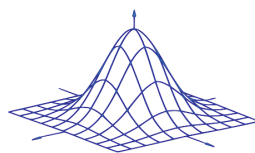
P5.23 Seminar room, Department of Physics, 5<sup>th</sup> Floor

**Abstract:** Field Programmable Gate Arrays (FPGAs) are a type of integrated circuit which may be configured post manufacture. They typically consist of thousands of independent logic blocks which may be configured to perform highly parallel computation. Historically these devices have been most often used in embedded scenarios such as telecommunications and networking where they have been typically programmed by electronic engineers in a hardware description language. I will discuss how, by adding an extra layer of abstraction, FPGAs may be programmed by physical scientists, as well as some of the novel programming paradigms and challenges that arise when implementing numerical methods on these devices.

A buffet lunch is available from 12:45 pm.



More info: <http://warwick.ac.uk/wcpm/seminars>



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